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APPLICATION NUMBER   FILING or 371(c)   GRP ART UNIT   FIL FEE REC'D   ATTY.DOCKET.NO   TOT CLAIMS   IND CLAIMS	10/595.303	05/19/2006	2812	1030	60291.000048	20	3
	APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS

# CONFIRMATION NO. 7114 FILING RECEIPT

21967 HUNTON & WILLIAMS LLP INTELLECTUAL PROPERTY DEPARTMENT 1900 K STREET, N.W. SUITE 1200 WASHINGTON. DC20006-1109

Date Mailed: 08/27/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filling Receipt, please write to the Office of Initial Patent Examination's Filling Receipt Corrections. Please provide a copy of this Filling Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filling Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filling Receipt incorporating the requested corrections

Applicant(s)

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Assignment For Published Patent Application

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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/DE04/02413 10/29/2004

Foreign Applications

GERMANY 103 50 460.5 10/29/2003

If Required, Foreign Filing License Granted: 08/22/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is

US10/595,303

Projected Publication Date: 11/29/2007

Non-Publication Request: No

Early Publication Request: No

Method and Device For Secure, Insulated and Electrically Conductive Assembling Of Treated Semiconductor Wafers

# **Preliminary Class**

438

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